



## ROAD RUNNER

# Road Runner

BitFlow designed the Road Runner from the ground up to support demanding digital imaging applications. Since its initial development, the Road Runner has been designed into hundreds of successful applications and continues to solve the most demanding interface and application requirements.

There are a number of different models which are now all out of production.

# Specifications

- 32-bit/33MHz bus master PCI bus interface board
- Flow-Thru architecture featuring a Scatter/Gather DMA engine that supports the direct transfer of data to VGA or system memory in real-time with no latency or CPU usage
- Acquires image sizes up to 512K by 32K pixels (vertical size is unlimited for line scan cameras)
- LVDS/RS422 (32-bit) area or line scan camera interface supporting a single camera with up to 4 8-bit channels, two synchronized cameras or two multiplexed asynchronous cameras (model dependent)
- 9 user-programmable I/O signals (4 inputs and 5 outputs)
- Multiple trigger modes
- Up to 40MHz acquisition for LVDS and 30MHz for RS-422 (contact us for higher clock rates)
- On-the-fly reformatting for multi-tap cameras
- Standard 8-bit in/12-bit out LUT
- Optional 16-bit in/16-bit out LUT available
- Cables and configuration files for more than 200 industrial cameras
- Drivers and DLLs for Windows NT, 2000 and XP
- Supported by all versions of the BitFlow SDK

## Models (limited quantities available)

### **RUN-PCI-11 / 11-M**

#### **(contact sales for availability)**

- One internal channel
- 8-bit input
- 8-bit in/12-bit out LUT
- Up to 40 MHz pixel clock

### **RUN-PCI-14 / 14-M**

#### **(contact sales for availability)**

- One internal channels
- 32-bit input
- 8-bit in/12-bit out LUT
- Up to 40 MHz pixel clock

# Models (no longer available)

## **RUN-PCI-12 / 12-M**

- One internal channel
- 16-bit input
- 8-bit in/12-bit out LUT
- Up to 40 MHz pixel clock

## **RUN-PCI-24 / 24-M**

- Two internal channels
- Scan reversal
- 32-bit input
- 8-bit in/12-bit out LUT
- Up to 40 MHz pixel clock

## **RUN-PCI-44 / 44-M**

- Four internal channels
- Scan reversal
- 32-bit input
- 8-bit in/12-bit out LUT
- Up to 40 MHz pixel clock